

**INFORMATION DISCLOSURE
STATEMENT**

SEP 03 2003

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 Serial No.: 10/615,446
 Applicant: RIZZOTTO ET AL.
 Filing Date: July 8, 2003
 Group:

U.S. PATENT DOCUMENTS

Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub Class	Translation
TM	AK	1083520	3/14/01	EP	G06N1	00	
TM	AL	01/67186	9/13/01	WO	G05B12	02	
	AM						
	AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

TM	AO	Hioe et al., A Single Flux Quantum Logic Gate with High Functionality, Superconductor Science and Technology, IOP Publishing, Techno House, Bristol, GB, Vol. 4, No. 11, November 1, 1991, Pages 658-661, XP000561158
TM	AP	Miquel et al., Factoring in a Dissipative Quantum Computer, Physical Review, A, General Physics, American Institute of Physics, New York, US, Vol. 54, No. 4, October 4, 1996, pages 2605-2613, XP002181469
TM	AQ	Verdal et al., Basic of Quantum Computation, Progress in Quantum Electronics, Pergamon Press, Oxford, GB, Vol. 22, February 1998, Pages 1-39, XP000937713
	AR	

EXAMINER: /Tan Mai/

DATE CONSIDERED: 06/27/2006

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.